



**Title:** Non-island style interconnect architecture for eFPGAs

**Abstract:** Non-island style interconnect architecture for eFPGAs eFPGAs (embedded FPGAs) are FPGA IP cores integrated into SoCs. Conventional eFPGAs employ island-style interconnects for routing architecture, which leads to large area overhead.

In this talk, I will present a non-island-style interconnect architecture that leverages the characteristics of gate-level netlists and show preliminary experimental results.